

Day : Thursday
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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = IWASAKI

First Name = MOTOYA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07213368</u>	<u>4853642</u>	150	06/30/1988	PHASE CONTROLLED DEMODULATOR FOR DIGITAL COMMUNICATIONS SYSTEM	IWASAKI, MOTOYA
<u>07390040</u>	<u>5012491</u>	150	08/07/1989	PREAMBLE DETECTION CIRCUIT FOR DIGITAL COMMUNICATIONS SYSTEM	IWASAKI, MOTOYA
<u>07418502</u>	Not Issued	161	10/10/1989	COHERENT DEMODULATING ARRANGEMENT FOR USE IN DIGITAL RADIO COMMUNICATIONS SYSTEM	IWASAKI, MOTOYA
<u>07435084</u>	Not Issued	166	11/13/1989	COHERENT PSK DEMODULATOR USING BIT ERROR RATE AND S/N RATIO TO ESTABLISH SYNCHRONIZATION	IWASAKI, MOTOYA
<u>07612087</u>	<u>5148451</u>	150	11/09/1990	CARRIER REGENERATING DEVICE CORRECTLY OPERABLE IN MOBILE SATELLITE COMMUNICATION	IWASAKI, MOTOYA
<u>07643793</u>	<u>5245612</u>	150	01/22/1991	SPREAD PACKET COMMUNICATION SYSTEM	IWASAKI, MOTOYA
<u>07705747</u>	<u>5128626</u>	150	05/28/1991	COHERENTLY DEMODULATING ARRANGEMENT INCLUDING QUASI-COHERENT DEMODULATOR FOR PSK SIGNALS	IWASAKI, MOTOYA
<u>07794421</u>	<u>5260896</u>	250	11/19/1991	ADAPTIVE FILTER AND A METHOD OF PREVENTING DIVERGENT BEHAVIOR OF THE ADAPTIVE FILTER	IWASAKI, MOTOYA
<u>07824716</u>	<u>5157694</u>	150	01/17/1992	COHERENT PSK DEMODULATOR USING BIT ERROR RATE AND S/N RATIO TO ESTABLISH	IWASAKI, MOTOYA

				SYNCHRONIZATION	
<u>07921711</u>	<u>5276710</u>	250	07/30/1992	CARRIER FREQUENCY ERROR DETECTOR CAPABLE OF ACCURATELY DETECTING A CARRIER FREQUENCY ERROR	IWASAKI, MOTOYA
<u>07990673</u>	<u>5463401</u>	150	12/14/1992	METHOD AND ARRANGEMENT OF POINTING AN ANTENNA BEAM TO A SITATIONARY SATELLITE	IWASAKI, MOTOYA
<u>08019142</u>	<u>5363415</u>	250	02/17/1993	CARRIER REGENERATING DEVICE CORRECTLY OPERABLE IN MOBILE SATELLITE COMMUNICATION	IWASAKI, MOTOYA
<u>08157698</u>	<u>5384552</u>	150	11/24/1993	CLOCK RECOVERY CIRCUIT FOR EXTRACTING CLOCK INFORMATION FROM A RECEIVED BASEBAND SIGNAL	IWASAKI, MOTOYA
<u>08255367</u>	<u>5500878</u>	150	06/08/1994	AUTOMATIC FREQUENCY CONTROL APPARATUS AND METHOD THEREFOR	IWASAKI, MOTOYA
<u>08305888</u>	<u>5440267</u>	150	09/14/1994	DEMODULATOR	IWASAKI, MOTOYA
<u>08363940</u>	<u>5513222</u>	150	12/27/1994	COMBINING CIRCUIT FOR A DIVERSITY RECEIVING SYSTEM	IWASAKI, MOTOYA
<u>08576968</u>	<u>5838797</u>	150	12/26/1995	SECURE COMMUNICATION BY ENCRYPTION/DECRYPTION OF VECTOR AT PSK MODULATION/DETECTION STAGE	IWASAKI, MOTOYA
<u>08825764</u>	<u>5912930</u>	250	04/01/1997	PHASE SHIFT KEYING SIGNAL DEMODULATION METHOD AND DEVICE	IWASAKI, MOTOYA
<u>08919820</u>	<u>6016329</u>	150	08/29/1997	METHOD AND APPARATUS FOR PREAMBLE-LESS DEMODULATION	IWASAKI, MOTOYA
<u>08980290</u>	<u>6088411</u>	150	11/28/1997	METHOD AND APPARATUS FOR A UNIQUE WORD DIFFERENTIAL DETECTION AND DEMODULATION USING THE UNIQUE WORD DIFFERENTIAL DETECTION	IWASAKI, MOTOYA
<u>09034400</u>	Not Issued	161	03/04/1998	METHOD AND APPARATUS FOR ORTHOGONAL FREQUENCY CONVERSION	IWASAKI, MOTOYA
<u>09038612</u>	<u>6147632</u>	150	03/11/1998	SAMPLING FREQUENCY	IWASAKI,

				CONVERSION APPARATUS AND FRACTIONAL FREQUENCY DIVIDING APPARATUS FOR SAMPLING FREQUENCY CONVERSION	MOTOYA
<u>09129614</u>	6108679	150	08/05/1998	DISCONTINUOUS SIGNAL INTERPOLATION CIRCUIT	IWASAKI, MOTOYA
<u>09325233</u>	Not Issued	168	06/03/1999	SAMPLING FREQUENCY CONVERSION APPARATUS AND FRACTIONAL FREQUENCY DIVIDING APPARATUS FOR SAMPLING FREQUENCY CONVERSIONS	IWASAKI, MOTOYA
<u>09405008</u>	6232761	150	09/27/1999	FREQUENCY ESTIMATING SYSTEM	IWASAKI, MOTOYA
<u>09505662</u>	6888812	150	02/17/2000	CDMA RECEIVER	IWASAKI, MOTOYA
<u>09533601</u>	6249235	150	03/23/2000	Sampling frequency conversion apparatus and fractional frequency dividing apparatus for sampling frequency conversion	IWASAKI, MOTOYA
<u>10088553</u>	6990141	150	03/19/2002	CORRELATOR	IWASAKI, MOTOYA
<u>10199013</u>	7085311	150	07/22/2002	APPARATUS AND METHOD FOR MEASURING SIR IN CDMA COMMUNICATION SYSTEM	IWASAKI, MOTOYA
<u>10206673</u>	7068713	150	07/29/2002	DIGITAL FILTER CIRCUIT	IWASAKI, MOTOYA
<u>10271722</u>	6993294	150	10/17/2002	MOBILE COMMUNICATION SYSTEM, COMMUNICATION CONTROL METHOD, BASE STATION AND MOBILE STATION TO BE USED IN THE SAME	IWASAKI, MOTOYA
<u>10304850</u>	6999734	150	11/27/2002	NONLINEAR COMPENSATING CIRCUIT, BASE-STATION APPARATUS, AND TRANSMISSION POWER CLIPPING METHOD	IWASAKI, MOTOYA
<u>10607149</u>	Not Issued	41	06/27/2003	Nonlinear distortion compensating circuit	IWASAKI, MOTOYA
<u>10642614</u>	Not Issued	30	08/19/2003	Amplitude limiting circuit and CDMA communication apparatus	IWASAKI, MOTOYA
<u>10884221</u>	Not Issued	30	07/02/2004	Non-linear compensation circuit, transmission apparatus and non-linear compensation method	IWASAKI, MOTOYA

<u>11071236</u>	Not Issued	30	03/04/2005	Communication system and apparatus, and control method therefor	IWASAKI, MOTOYA
<u>11102672</u>	Not Issued	41	04/11/2005	Mobile communication system, base station terminal, and control method therefor	IWASAKI, MOTOYA
<u>11265308</u>	Not Issued	30	11/03/2005	Radio communications system and control method therefor	IWASAKI, MOTOYA
<u>11283855</u>	Not Issued	30	11/22/2005	Active set selection method for determining cells to be deleted from an active set based on the reception state of an uplink high-speed signal	IWASAKI, MOTOYA
<u>11311347</u>	Not Issued	30	12/20/2005	Communication system and transmitter-receiver for use therewith	IWASAKI, MOTOYA
<u>11378250</u>	Not Issued	30	03/20/2006	Mobile communications system, radio network controller, and active set control method	IWASAKI, MOTOYA

Inventor Search Completed: No Records to Display.

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